

**Notice of References Cited**

Application/Control No.

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Applicant(s)/Patent Under

Reexamination

JAHNKE ET AL.

Examiner

Kim T. Huynh

Art Unit

2112

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,621,898	04-1997	Wooten, David R.	710/117
	B	US-5,572,230	11-1996	Takano et al.	345/68
	C	US-6,173,348	01-2001	Hewitt, Larry	710/107
	D	US-6,199,132	03-2001	Hewitt et al.	710/107
	E	US-6,426,032	07-2002	Osuka et al.	264/328.9
	F	US-6,651,119	11-2003	Ghodrat et al.	710/40
	G	US-6,681,279	01-2004	Peng, Sheng-Chang	710/240
	H	US-6,571,306	05-2003	Smith, Brian L.	710/240
	I	US-6,463,491	10-2002	Furuta et al.	710/240
	J	US-5,577,214	11-1996	Bhattacharya, Dipankar	710/107
	K	US-2001/0056515	12-2001	JACOBS et al.	710/240
	L	US-2003/0005194	01-2003	Stone et al.	710/107
	M	US-2002/0032820	03-2002	Jahnke et al.	710/240

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP2001117861	10-1999	Japan	Hamamoto Yasuo	G06F 13/362
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	A scheme for knowledge representation, verification and reasoning in real time asynchronous production systems. T. Siva Perraju, 1063-6730/94 @1994 IEEE
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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